

# **Toward wafer-scale integration of graphene single crystals arrays**

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The integration of graphene in technologically relevant architectures poses major challenges to the 2D materials community. A basic requirement is the ability of (i) growing, (ii) transferring and (iii) processing graphene over large scale without compromising its outstanding electronic properties. We will present the latest development of our research in these three areas: deterministic “seeded” CVD growth of graphene single crystals arrays on Cu, semi-dry transfer on arbitrary substrates and fabrication of opto-electronic devices.

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