

University of Modena and Reggio Emilia.

He was at the University of California at Berkeley (USA) from 1992 to 1994. He received his PhD from the University of Padova in 1994. In 1997-1999 he visited Saifun Semiconductors (Israel) for research activity on NROM, one of the first innovative charge trapping nonvolatile memories. His research interests are in the characterization, modeling and reliability of nonvolatile memories and innovative devices. He is also active in the industrial electronic applications field, namely “by-wire” and wireless embedded systems, energy harvesting devices and circuits, for automotive applications as well.

He has been member of Technical Committees in many Conferences and Meetings (IEDM, IRPS, ESSDERC, ESREF, VLSI-TSA). He has been the Technical Program Chair of ESSDERC 2014. He is currently a member of the Steering Committee of ESSDERC. He has been Guest Editor of IEEE Transactions on Device and Material Reliability and Solid State Electronics. He is Associate Editor of IEEE Journal of the Electron Device Society.